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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10001478
	Filing Date		2001-11-01
	First Named Inventor	Craig Nemecek	
	Art Unit	2123	
	Examiner Name	<del>Jason Scott Pector</del> /Dwin Craig/	
	Attorney Docket Number	CD01213M	

## U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/D.C./	1	6910126	B1	2005-06-21	Mar et al.	Entire Document
/D.C./	2	6825689	B1	2004-11-30	Snyder	Entire Document
/D.C./	3	6798299	B1	2004-09-28	Mar et al.	Entire Document
/D.C./	4	7023257	B1	2006-04-04	Sullam	Entire Document
/D.C./	5	6967511	B1	2005-11-22	Sullam	Entire Document
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/D.C./	7	6823282	B1	2004-11-23	Snyder	Entire Document
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Attorney Docket Number	CD01213M	

/D.C./	9	6611220	B1	2003-08-26	Snyder	Entire Document
/D.C./	10	7180342	B1	2007-02-20	Shutt et al.	Entire Document
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/D.C./	20	6981090	B1	2005-12-27	Kutz et al.	Entire Document
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/D.C./	32	6608472	B1	2003-08-19	Kutz et al.	Entire Document
/D.C./	33	7185321	B1	2007-02-27	Roe et al.	Entire Document

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**U.S. PATENT APPLICATION PUBLICATIONS**

Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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/D.C./	1	USPTO Notice of Allowance for 10/238,966 (CD01250) dated 01/27/09; 4 pages	<input type="checkbox"/>



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/D.C./	2	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 06/30/08; 12 pages	<input type="checkbox"/>
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/D.C./	7	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 04/06/06; 8 pages	<input type="checkbox"/>
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/D.C./	10	USPTO Final Rejection for 10/033,027 (CD00232) dated 06/08/07; 8 pages	<input type="checkbox"/>
/D.C./	11	USPTO Non-Final Rejection for 10/033,027 (CD00232) dated 12/21/06; 31 pages	<input type="checkbox"/>
/D.C./	12	USPTO Final Rejection for 10/033,027 (CD00232) dated 08/09/06; 6 pages	<input type="checkbox"/>

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/D.C./	13	USPTO Non-Final Rejection for 10/033,027 (CD00232) dated 04/26/06; 26 pages	<input type="checkbox"/>
/D.C./	14	USPTO Final Rejection for 10/033,027 (CD00232) dated 10/31/05; 24 pages	<input type="checkbox"/>
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	18	<del>Application Number 09/842,966 (CD00195): "Precision Crystal Oscillator Circuit Used in Microcontroller," Ma; 20 pages</del>	<input type="checkbox"/>
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/D.C./	21	"Micropower CMOS Temperature Sensor with Digital Output," Bakker et al., IEEE Journal of Solid-State Circuits, 1996; 3 pages	<input type="checkbox"/>
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/D.C./	24	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 03/02/06; 14 pages	<input type="checkbox"/>
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/D.C./	31	USPTO Final Rejection for 10/002,217 (CD01207) dated 03/07/07; 12 pages	<input type="checkbox"/>
/D.C./	32	USPTO Non-Final Rejection for 10/002,217 (CD01207) dated 10/02/06; 21 pages	<input type="checkbox"/>
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/D.C./	35	USPTO Non-Final Rejection for 10/002,217 (CD01207) dated 05/19/05; 15 pages	<input type="checkbox"/>
/D.C./	36	USPTO Notice of Allowance for 10/001,477 (CD01203) dated 11/10/08; 7 pages	<input type="checkbox"/>
/D.C./	37	USPTO Advisory Action for 10/001,477 (CD01203) dated 10/10/08; 3 pages	<input type="checkbox"/>
/D.C./	38	USPTO Final Rejection for 10/001,477 (CD01203) dated 06/30/08; 15 pages	<input type="checkbox"/>
/D.C./	39	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 12/06/07; 13 pages	<input type="checkbox"/>
/D.C./	40	USPTO Final Rejection for 10/001,477 (CD01203) dated 07/23/07; 16 pages	<input type="checkbox"/>
/D.C./	41	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 01/22/07; 15 pages	<input type="checkbox"/>

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**EXAMINER SIGNATURE**

Examiner Signature	/Dwin Craig/	Date Considered	10/13/2011
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**U.S. PATENTS**

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/D.C./	9	6249167	B1	2001-06-19	Oguchi et al.	Entire Document
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/D.C./	20	5331571	B1	1994-07-19	Aronoff et al.	Entire Document
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/D.C./	31	5428319	B1	1995-06-27	Marvin et al.	Entire Document
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/D.C./	43	5140197	B1	1992-08-18	Grider	Entire Document
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/D.C./	48	5546433	B1	1996-08-13	Tran et al.	Entire Document

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**U.S. PATENT APPLICATION PUBLICATIONS**

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/D.C./	1	PCT/US99/09712	WO	A1	1999-02-25	Schutte	Entire Document	<input type="checkbox"/>
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( Not for submission under 37 CFR 1.99)

Application Number		10001478
Filing Date		2001-11-01
First Named Inventor	Craig Nemecek	
Art Unit	2123	
Examiner Name	<del>Jacob Scott Proctor</del> /Dwin Craig/	
Attorney Docket Number	CD01213M	

/D.C./	17	USPTO Non-Final Rejection for 09/975,338 (CD00186) dated 04/05/05; 13 pages	<input type="checkbox"/>
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/D.C./	21	USPTO Final Rejection for 10/008,096 (CD01212M) dated 10/13/06; 17 pages	<input type="checkbox"/>
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/D.C./	23	USPTO Non-Final Rejection for 10/008,096 (CD01212M) dated 03/07/07; 19 pages	<input type="checkbox"/>
/D.C./	24	USPTO Non-Final Rejection for 10/008,096 (CD01212M) dated 04/17/06; 18 pages	<input type="checkbox"/>
/D.C./	25	USPTO Non-Final Rejection for 10/008,096 (CD01212M) dated 06/14/04; 24 pages	<input type="checkbox"/>
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	33	Application Number 09/943,062 (CD00228): "Apparatus and Method for Programmable Power Management in a Programmable Analog Circuit Block," Mar, 46 pages	<input type="checkbox"/>
	34	Application Number 10/238,966 (CD01250M): "Method for Parameterizing a User Module," Penin, 41 pages	<input type="checkbox"/>
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/D.C./	39	USPTO Non-Final Rejection for 09/998,859 (CD01209M) dated 11/04/2004; 6 pages	<input type="checkbox"/>
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	48	<del>Application Number 09/207,912 (CD98086): "Circuit(s), Architecture and Method(s) for Operating and/or Tuning a Ring Oscillator," Mar; 23 pages</del>	<input type="checkbox"/>
/D.C./	49	USPTO Notice of Allowance for Application Number 10/001,477 (CD1203M) dated 05/08/2009; 6 pages	<input type="checkbox"/>

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**U.S. PATENTS**

Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/D.C./	1	6681280	B1	2004-01-20	Miyake, et al.	Entire Document
/D.C./	2	5399922	B1	1995-03-21	Kiani et al.	Entire Document
/D.C./	3	5680070	B1	1997-10-21	Anderson et al.	Entire Document
/D.C./	4	6314530	B1	2001-11-06	Mann	Entire Document
/D.C./	5	5438672	B1	1995-08-01	Dey	Entire Document
/D.C./	6	6166367	B1	2000-12-26	Cho	Entire Document
/D.C./	7	5600262	B1	1997-02-04	Kolze	Entire Document
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/D.C./	9	5258760	B1	1993-11-02	Moody et al.	Entire Document
/D.C./	10	5563526	B1	1996-10-08	Hastings et al.	Entire Document
/D.C./	11	6225866	B1	2001-05-01	Kubota et al.	Entire Document
/D.C./	12	5880598	B1	1999-03-09	Duong	Entire Document
/D.C./	13	6304101	B1	2001-10-16	Nishihara	Entire Document
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/D.C./	15	5828693	B1	1998-10-27	Mays et al.	Entire Document
/D.C./	16	5703871	B1	1997-12-30	Pope et al.	Entire Document
/D.C./	17	6018559	B1	2000-01-25	Azegami et al.	Entire Document
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Examiner Name	<del>Jesse Scott Proctor</del> /Dwin Craig/	
Attorney Docket Number	CD01213M	

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/D.C./	21	4138671	B1	1997-02-06	Comer et al.	Entire Document
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/D.C./	23	5050168	B1	1991-09-17	Paterson	Entire Document
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/D.C./	27	6263302	B1	2001-07-17	Hellestrand, et al.	Entire Document
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	1	0532478	WO	A1	1995-11-20	Klien	Entire Document	<input type="checkbox"/>
/D.C./	2	0450863A2	EP	A1	1991-10-09	Austin	Entire Document	<input type="checkbox"/>
/D.C./	3	0499383A2	EP	A1	1992-08-19	El Ayat, Khaled, A.	Entire Document	<input type="checkbox"/>
/D.C./	4	0308583A2	EP	A1	1989-03-29	Bartlett	Entire Document	<input type="checkbox"/>
/D.C./	5	0639816A2	EP	A1	1995-02-22	McCollum	Entire Document	<input type="checkbox"/>
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/D.C./	7	1205848	EP	A1	2002-05-15	Ericsson	Entire Document	<input type="checkbox"/>
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	<del>9</del>	<del>404083465</del>	<del>JP</del>	<del>A1</del>	<del>1992-03-17</del>	<del>Yasue</del>	<del>Entire Document</del>	<input type="checkbox"/>
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Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>5</sup>
/D.C./	1	From Application Number 10/033,027 (CD00232): Goodenough, F. "Analog Counterparts of FPGAS Ease System Design" Electronic Design, Penton Publishing, Cleveland, OH, US vol. 42, no. 21, 10/14/94; 10 pages	<input type="checkbox"/>
/D.C./	2	From Application Number 10/033,027 (CD00232): Harbaum, T. et al. "Design of a Flexible Coprocessor Unit" Proceedings of the Euromicro Conference, XX XX, Sept. 1999; 10 pages	<input type="checkbox"/>
/D.C./	3	From Application Number 10/033,027; CD00169: "Programmable Microcontroller (PSoC) Architecture (Mixed Analog/Digital)"; 08/07/01; 09/924,734 Snyder et al.; 28 pages	<input type="checkbox"/>
/D.C./	4	From Application Number 10/033,027; CD00170: "Digital Configurable Macro Architecture"; 07/18/01; 09/909,045; Snyder; 37 pages	<input type="checkbox"/>

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/D.C./	5	From Application Number 10/033,027: CD00172; "Configuring Digital Functions in a Digital Configurable Macro Architecture"; 07/18/01; 09/909,109; Snyder; 38 pages	<input type="checkbox"/>
/D.C./	6	From Application Number 10/033,027: CD00173; "A Programmable Analog System Architecture (As Amended)"; 07/18/01; 09/909,047; Mar; 60 pages	<input type="checkbox"/>
/D.C./	7	From Application Number 10/033,027: CD00174; "Programmable Methodology and Architecture for a Programmable Analog System (As Amended)"; 08/14/01; 09/930,021; Mar et al.; 87 pages	<input type="checkbox"/>
/D.C./	8	From Application Number 10/033,027: CD00175; "Method for Synchronizing and Resetting Clock Signals Supplied to Multiple Programmable Analog Blocks (As Amended)"; 10/01/01; 09/969,311; Sullam; 57 pages	<input type="checkbox"/>
/D.C./	9	From Application Number 10/033,027: CD00180; "Method and Apparatus for Programming a Flash Memory"; 06/05/01; 09/875,599; Snyder; 23 pages	<input type="checkbox"/>
/D.C./	10	From Application Number 10/033,027: CD00182; "In-System Chip Emulator Architecture"; 10/10/01; 09/975,115; Snyder et al.; 38 pages	<input type="checkbox"/>
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/D.C./	14	From Application Number 10/033,027 (CD00227): "Architecture for Synchronizing and Resetting Clock Signals Supplied to Multiple Analog Programmable Analog Blocks"; 10/01/01; 09/969,313; Sullam; 50 pages	<input type="checkbox"/>
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/D.C./	19	HINTZ ET AL.; "MICROCONTROLLERS", 1992, McGraw-Hill; 11 pages	<input type="checkbox"/>
/D.C./	20	GANAPATHY, GOPI, AND NARAYAN, RAM, AND JORDEN, GLEN, AND FERNANDEZ, DENZIL, AND WANG, MING, AND NISHIMURA, JIM; "Hardware Emulation for Functional Verification of K5", June 1996, 33rd Design Automation Conference Proceedings, 3-7 June 1996; 4 pages	<input type="checkbox"/>
	21	<del>The Provisional Application 60/243,708 (CD00167D); "Advanced Programmable Microcontroller Device"; 277 pages</del>	<input type="checkbox"/>
/D.C./	22	"Webster's Third New International Dictionary", 1996, G. & C. Merriam Company; 3 pages	<input type="checkbox"/>
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	24	<del>Application Number 09/957,884 (CD00204); "A Crystal-Less Oscillator with Trimmable Analog Current Control for Increased Stability;" Mar; 28 pages</del>	<input type="checkbox"/>
	25	<del>Application Number 09/969,313 (CD00227); "Architecture for Synchronizing and Resetting Clock Signals Supplied to Multiple Analog Programmable System on a Chip Block;" Sullam; 50 pages</del>	<input type="checkbox"/>
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27	Application Number 09/972,003 (CD00176): "Test Architecture for Microcontroller Providing for a Serial Communication Interface;" Snyder; 32 pages	<input type="checkbox"/>
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32	Application Number 11/125,554 (CD00230C): "A Method for a Efficient Supply to a Microcontroller;" Kutz; 1 page	<input type="checkbox"/>
33	Application Number 09/855,868 (CD00181): "Protecting Access to Microcontroller Memory Blocks;" Snyder; 28 pages	<input type="checkbox"/>
34	Application Number 09/967,923 (CD00229): "Novel Method and System for Interacting between a Processor and a Power on Reset to Dynamically Control Power States in a Microcontroller;" Kutz; 44 pages	<input type="checkbox"/>
35	Application Number 10/000,383 (CD00189): "System and Method of Providing a Programmable Clock Architecture for an Advanced Microcontroller;" Sullam; 34 pages	<input type="checkbox"/>
36	Application Number 10/001,477 (CD01203M): "Breakpoint Control in an In-Circuit Emulation System;" Roe; 43 pages	<input type="checkbox"/>
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38	Application Number 10/004,039 (CD01222M): "In-Circuit Emulator with Gatekeeper for Watchdog Timer;" Nemecek; 46 pages	<input type="checkbox"/>
39	Application Number 10/002,217 (CD01207M): "Conditional Branching in an In-Circuit Emulation System;" Nemecek; 43 pages	<input type="checkbox"/>
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42	Application Number 09/887,955 (CD00197): "Novel Power on Reset Circuit for Microcontroller;" Kutz; 42 pages	<input type="checkbox"/>
43	Application Number 09/826,397 (CD00190): "Method and Circuit for Allowing a Microprocessor to Change its Operating Frequency on-the-fly;" Sullam; 24 pages	<input type="checkbox"/>
44	Application Number 09/893,048 (CD00231): "A Microcontroller having an On-Chip High Gain Amplifier;" Kutz; 22 pages	<input type="checkbox"/>
45	Application Number 09/912,768 (CD00200): "A Microcontroller having a Dual Mode Relax Oscillator that is Trimmable;" Shutt; 33 pages	<input type="checkbox"/>
46	Application Number 09/922,419 (CD00198): "A Power Supply Pump Circuit for a Microcontroller;" Kutz; 38 pages	<input type="checkbox"/>
47	Application Number 09/922,579 (CD00230): "A Method for a Efficient Supply to a Microcontroller;" Kutz; 37 pages	<input type="checkbox"/>
48	Application Number 09/923,461 (CD00192): "Non-Interfering Multiply-Mac (Multiply Accumulate) Circuit;" Snyder; 25 pages	<input type="checkbox"/>

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49	Application Number 09/935,454 (CD00104); "Method and Apparatus for Local and Global Power Management in a Programmable Analog Circuit;" Mar; 51 pages	<input type="checkbox"/>
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/D.C./	7	Oh et al.; "Emulator Environment Based on an FPGA Prototyping Board"; IEEE 21-23; June 2000; 6 pages	<input type="checkbox"/>
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/D.C./	10	Pasternak; "In-Circuit-Emulation in ASIC Architecture Cor Designs"; IEEE 1989; 4 pages	<input type="checkbox"/>
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Application Number	10001478
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First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	<del>Jean Scott Proctor</del> /Dwin Craig/
Attorney Docket Number	CD01213M

/D.C./	12	WALTERS, STEPHEN; "Computer-Aided Prototyping for ASIC-Based Systems", 1991, IEEE Design & Test of Computers; Volume 8, Issue 2; 8 pages	<input type="checkbox"/>
↑	13	Anonymous; "JEEN JTAG Embedded Ice Ethernet Interface", August 1999; Embedded Performance, Inc.; 3 pages	<input type="checkbox"/>
	14	Sedory; "A Guide to Debug"; 2004; retrieved on May 20, 2005; 7 pages	<input type="checkbox"/>
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	16	Xerox; "Mesa Debugger Documentation"; April 1979; Xerox Systems Development Department; Version 5.0; 33 pages	<input type="checkbox"/>
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	21	USPTO Non-Final Rejection for 09/989,767 (CD01178M) dated 10/06/04; 15 pages	<input type="checkbox"/>
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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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	9	6169383	B1	2001-01-02	Sabin et al.	Entire Document
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	12	6453461	B1	2002-09-17	Chaiken	Entire Document
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/D.C./	20	5339262	B1	1994-08-16	Rostoker, et al.	Entire Document
↑	21	5455927	B1	1995-10-03	Huang	Entire Document
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/D.C./	5	USPTO Non-Final Rejection for 09/989,767 (CD01178M) dated 07/24/08; 21 pages	<input type="checkbox"/>
/D.C./	6	Anonymous; "Using Debug"; 1999; Prentice-Hall Publishing; 20 pages	<input type="checkbox"/>
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/D.C./	8	Microsoft Press Computer User's Dictionary; 1998; 3 pages	<input type="checkbox"/>
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	10	6384947	B1	2002-05-07	Ackerman, et al.	Entire Document
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Application Number		10001478
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First Named Inventor	Craig Nemecek	
Art Unit		2123
Examiner Name	<del>Jason Scott Proctor</del> /Dwin Craig/	
Attorney Docket Number		CD01213M

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/D.C./	45	USPTO Final Rejection for 09/989,777 (CD01208) dated 12/21/2005; 29 pages	<input type="checkbox"/>
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Application Number	10001478
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First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	<del>Jason Scott Proctor</del> /Dwin Craig/
Attorney Docket Number	CD01213M

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/D.C./	36	6026501	B1	2000-02-15	Hohl et al.	Entire Document
/D.C./	37	6567932	B1	2003-05-20	Edwards et al.	Entire Document
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**INFORMATION DISCLOSURE  
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Application Number	10001478
Filing Date	2001-11-01
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	<del>Jesse Scott Proctor</del> /Dwin Craig/
Attorney Docket Number	CD01213M

/D.C./	42	5875293	B1	1999-02-23	Bell et al.	Entire Document
/D.C./	43	5663965	B1	1997-09-02	Seymour	Entire Document
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**U.S. PATENT APPLICATION PUBLICATIONS**

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/D.C./	1	USPTO Notice of Allowance for Application Number 09/930,021 (CD00174) dated 11/26/2004; 4 pages	<input type="checkbox"/>
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Attorney Docket Number	CD01213M

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Attorney Docket Number	CD01213M	

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Examiner Name	<del>Jesse Scott Pector</del> /Dwin Craig/
Attorney Docket Number	CD01213M

/D.C./	50	USPTO Non-Final Rejection for Application Number 09/887,923 (CD00229) dated 05/25/2004; 7 pages	<input type="checkbox"/>
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Examiner Name	<del>Jason Scott Pester</del> /Dwin Craig/
Attorney Docket Number	CD01213M

**U.S. PATENTS**

Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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Art Unit	2123
Examiner Name	<del>Jason Scott Proctor</del> /Dwin Craig/
Attorney Docket Number	CD01213M

/D.C./	9	6396302	B1	2002-05-28	New et al.	Entire Document
/D.C./	10	6204687	B1	2001-03-20	Schultz et al.	Entire Document
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/D.C./	14	5949632	B1	1999-09-07	Barreras Sr. et al.	Entire Document
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First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	<del>Jason Cook-Peters</del> /Dwin Craig/
Attorney Docket Number	CD01213M

/D.C./	20	6286127	B1	2001-09-04	King et al.	Entire Document
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/D.C./	25	6003133	B1	1999-12-14	Moughanni et al.	Entire Document
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/D.C./	31	6839774	B1	2005-01-04	Ahn et al.	Entire Document
/D.C./	32	6754765	B1	2004-06-22	Chang et al.	Entire Document
/D.C./	33	5630057	B1	1997-05-13	Hait	Entire Document
/D.C./	34	6324628	B1	2001-11-27	Chan	Entire Document
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/D.C./	36	6219729	B1	2001-04-17	Keats et al.	Entire Document
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/D.C./	43	5986479	B1	1999-11-16	Mohan, Jitendra	Entire Document
/D.C./	44	6603348	B1	2003-08-05	Preuss et al.	Entire Document

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**U.S.PATENT APPLICATION PUBLICATIONS**

Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/D.C./	1	20040153802	A1	2004-08-05	Kudo et al.	Entire Document
/D.C./	2	20020059543	A1	2002-05-16	Cheng et al.	Entire Document
/D.C./	3	20010010083	A1	2001-07-26	Satoh	Entire Document
/D.C./	4	20020133794	A1	2002-09-19	Kanapathippillai et al.	Entire Document
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/D.C./	19	USPTO Non-Final Rejection for Application Number 10/004,039 (CD01222M) dated 06/06/2005; 17 pages	<input type="checkbox"/>
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Art Unit	2123
Examiner Name	<del>Jason Scott Proctor</del> /Dwin Craig/
Attorney Docket Number	CD01213M

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Attorney Docket Number	CD01213M	

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/D.C./	2	20040054821	A1	2004-03-18	Warren et al.	Entire Document

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**FOREIGN PATENT DOCUMENTS**

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	8	Adham et al., "Preliminary Outline of the IEEE P1500 Scalable Architecture for Testing Embedded Cores," IEEE, 8 pages	<input type="checkbox"/>
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/D.C./	23	USPTO Final Rejection for Application Number 10/001,478 (CD01213M) dated 04/20/2009; 16 pages	<input type="checkbox"/>
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/D.C./	41	Application Number 09/998,859 (CD01209M): "A System and a Method for Checking Lock Step Consistency between in Circuit Emulation and a Microcontroller while Debugging Process is in Progress;" Nemecek; 33 pages	<input type="checkbox"/>
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Application Number	10001478
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First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	<del>Jesse Scott Proctor</del> /Dwin Craig/
Attorney Docket Number	CD01213M

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Art Unit	2123
Examiner Name	<del>Jason Scott-Reactor</del> /Dwin Craig/
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/D.C./	2	USPTO Non-Final Rejection for Application Number 11/850,260 (CD2007006) dated 03/06/2009; 7 pages	<input type="checkbox"/>
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/D.C./	6	USPTO Non-Final Rejection for Application Number 11/644,100 (CD05204) dated 04/14/2008; 10 pages	<input type="checkbox"/>
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Attorney Docket Number	CD01213M

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	Examiner Name	<del>Jason Scott Proctor</del> /Dwin Craig/	
	Attorney Docket Number	CD01213M	

/D.C./	50	USPTO Non-Final Rejection for Application Number 10/226,911 (CD02112) dated 03/19/2004; 6 pages	<input type="checkbox"/>
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/D.C./	3	USPTO Non-Final Rejection for Application Number 09/943,149 (CD01085) dated 08/28/2003; 9 pages	<input type="checkbox"/>
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/D.C./	5	USPTO Non-Final Rejection for Application Number 09/943,149 (CD01085) dated 11/20/2002; 7 pages	<input type="checkbox"/>
	6	Application Number 09/943,149 (CD01085); "Method for Phase Locking in a Phase Lock Loop," Moyal et al., 24 pages	<input type="checkbox"/>
/D.C./	7	Durham et al., "Integrated Continuous-Time Balanced Filters for 16-bit DSP Interfaces," IEEE, 1993; 6 pages	<input type="checkbox"/>

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Examiner Name	<del>Jason Scott Proctor</del> /Dwin Craig/
Attorney Docket Number	CD01213M

/D.C./	8	Durham et al., "Circuit Architectures for High Linearity Monolithic Continuous-Time Filtering," IEEE, 1992; 7 pages	<input type="checkbox"/>
/D.C./	9	Durham et al., "High-Linearity Conitnuous-Time Filter in 5-V VLSI CMOS," IEEE, 1992; 8 pages	<input type="checkbox"/>
	<del>10</del>	<del>Application Number 09/047,595 (CD97115): "Roving Range Control to Limit Receive PLL Frequency of Operation," Scott; 35 pages</del>	<del><input type="checkbox"/></del>
	<del>11</del>	<del>Application Number 09/216,460 (CD98099): "Circuit and Method for Controlling an Output of a Ring Oscillator," Abugharbieh et al.; 21 pages</del>	<del><input type="checkbox"/></del>
	<del>12</del>	<del>Application Number 09/471,914 (CD99064): "Reference-Free Clock Generator and Data Recovery PLL," Dalmia et al.; 32 pages</del>	<del><input type="checkbox"/></del>
	<del>13</del>	<del>Application Number 09/471,576 (CD99062): "Reference-Free Clock Generation and Data Recovery PLL," Dalmia; 30 pages</del>	<del><input type="checkbox"/></del>
	<del>14</del>	<del>Application Number 10/083,442 (CD01264): "Method/Architecture for a Low Gain PLL with Wide Frequency Range," Meyers et al.; 28 pages</del>	<del><input type="checkbox"/></del>
	<del>15</del>	<del>Application Number 09/470,655 (CD99065): "Digital Phase/Frequency Detector and Clock Generator and Data Recovery PLL Containing the Same," Dalmia; 26 pages</del>	<del><input type="checkbox"/></del>
	<del>16</del>	<del>Application Number 09/893,161 (CD00094): "Architecture of a PLL with Dynamic Frequency Control on a PLD," Moore; 32 pages</del>	<del><input type="checkbox"/></del>
	<del>17</del>	<del>Application Number 09/608,753 (CD00018): "PLL Lockout Watchdog," Wilson et al.; 24 pages</del>	<del><input type="checkbox"/></del>
	<del>18</del>	<del>Application Number 09/398,956 (CD99049): "Frequency Acquisition Rate Control in Phase Lock Loop Circuits," Moyal et al.; 35 pages</del>	<del><input type="checkbox"/></del>

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	<del>19</del>	<del>Application Number 08/747,262 (CD000005): "Linearized Digital Phase-Locked Loop," Williams et al.; 9 pages</del>	<input type="checkbox"/>
	<del>20</del>	<del>Application Number 09/001,440 (CD01102): "Oscillator Tuning Method," Hauck; 28 pages</del>	<input type="checkbox"/>
	<del>24</del>	<del>Application Number 06/536,606 (CD00104): "Memory Based Phase Locked Loop," Krishnan; 27 pages</del>	<input type="checkbox"/>
	<del>22</del>	<del>Application Number 09/048,905 (CD95045): "Programmable Clock Generator," Mann et al.; 42 pages</del>	<input type="checkbox"/>
	<del>23</del>	<del>Application Number 06/005,942 (CD00045C): "Programmable Clock Generator," Mann et al.; 41 pages</del>	<input type="checkbox"/>
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/D.C./	25	USPTO Non-Final Rejection for Application Number 10/293,392 (CD02135) dated 10/16/2003; 6 pages	<input type="checkbox"/>
	26	Application Number 10/282,392 (CD02135): "Low Voltage Receiver Circuit and Method for Shifting the Differential Input Signals of the Receiver Depending on a Common Mode Voltage of the Input Signals," Maher et al.; 20 pages	<input type="checkbox"/>
/D.C./	27	USPTO Notice of Allowance for Application Number 10/288,003 (CD02108) dated 01/14/2005; 6 pages	<input type="checkbox"/>
/D.C./	28	USPTO Final Rejection for Application Number 10/288,003 (CD02108) dated 10/06/2004; 6 pages	<input type="checkbox"/>
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00	Application Number 10/288 003 (CD02108): "Low Voltage Differential Signal Driver Circuit and Method." Roper et al. : 30 pages	<input type="checkbox"/>
31	USPTO Notice of Allowance for Application Number 00/989,777 (CD01208M) dated 06/03/2009, 7 pages	<input type="checkbox"/>

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## **U.S.PATENTS**

Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/D.C./	1	6411665	B1	2002-06-25	Chan et al.	Entire Document
↓	2	5867046	B1	1999-02-02	Sugasawa, Yasuo	Entire Document
↓	3	5517198	B1	1996-05-14	McEwan, Thomas E.	Entire Document
↓	4	5361290	B1	1994-11-01	Akiyama, Shin-ichiro	Entire Document
↓	5	6768352	B1	2004-07-27	Maher et al.	Entire Document
↓	6	6900663	B1	2005-05-31	Roper et al.	Entire Document
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/D.C./	9	5392784	B1	1995-02-28	Gudaitis	Entire Document
↑	10	6292028	B1	2001-09-18	Tomita	Entire Document
	11	6559685	B1	2003-05-06	Green	Entire Document
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	13	6111431	B1	2000-08-29	Estrada	Entire Document
	14	6590422	B1	2003-07-08	Dillon	Entire Document
	15	6600346	B1	2003-07-29	Macaluso	Entire Document
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/D.C./	20	4727541	B1	1988-02-23	Mori et al.	Entire Document
	21	4809345	B1	1989-02-28	Tabata et al.	Entire Document
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	25	5914633	B1	1999-06-22	Comino et al.	Entire Document
	26	6628163	B1	2003-09-30	Dathe et al.	Entire Document
	27	6677814	B1	2004-01-13	Low et al.	Entire Document
	28	5625316	B1	1997-04-29	Chambers et al.	Entire Document
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↑	32	6359950	B1	2002-03-19	Gossmann et al.	Entire Document
	33	6005904	B1	1999-12-21	Knapp et al.	Entire Document
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	35	5159292	B1	1992-10-27	Canfield et al.	Entire Document
	36	6366174	B1	2002-04-02	Berry et al.	Entire Document
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/D.C./	42	5043674	B1	1991-08-27	Bonaccio et al.	Entire Document
	43	5280199	B1	1994-01-18	Itakura	Entire Document
	44	6130548	B1	2000-10-10	Koifman	Entire Document
	45	6236242	B1	2001-05-22	Hedberg	Entire Document
	46	6281753	B1	2001-08-28	Corsi et al.	Entire Document
	47	6617888	B1	2003-09-09	Volk	Entire Document
	48	6107882	B1	2000-08-22	Gabara et al.	Entire Document
	49	6294932	B1	2001-09-25	Watarai, Seiichi	Entire Document
/D.C./	50	5544067	B1	1996-08-06	Rostoker et al.	Entire Document

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/D.C./	1	20030135842	A1	2003-07-17	Frey et al.	Entire Document
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/D.C./	1	USPTO Advisory Action for Application Number 11/200,619 (CD04112) dated 05/11/2009; 3 pages	<input type="checkbox"/>
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	4	Application Number 11/200,619 (CD04112): "Providing hardware independence to automatic code generation of processing device firmware," Snyder et al.; 41 pages	<input type="checkbox"/>
/D.C./	5	USPTO Notice of Allowance for Application Number 11/201,922 (CD04111) dated 04/09/2009; 4 pages	<input type="checkbox"/>

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	<del>9</del>	<del>Application Number 11/201,922 (CD04111): "Design model for a hardware device-independent method of defining embedded firmware for programmable systems," McDonald et al.; 31 pages</del>	<del><input type="checkbox"/></del>
/D.C./	10	USPTO Non-Final Rejection for Application Number 11/201,627 (CD04109) dated 12/12/2008; 17 pages	<input type="checkbox"/>
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	<del>13</del>	<del>Application Number 11/201,627 (CD04109): "Method and an apparatus to design a processing system using a graphical user interface," Ogami et al.; 37 pages</del>	<del><input type="checkbox"/></del>
/D.C./	14	USPTO Notice of Allowance for Application Number 09/989,808 (CD01169M) dated 02/13/2006; 4 pages	<input type="checkbox"/>
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	<del>17</del>	<del>Application Number 09/989,808 (CD01169M): "Automatic generation of application program interfaces, source code, interrupts, and data sheets for microcontroller programming," Bartz et al.; 67 pages</del>	<input type="checkbox"/>
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/D.C./	33	Wang, et al. "Synthesizing Operating System Based Device Drivers in Embedded Systems," 2003, ACM; 8 pages	<input type="checkbox"/>
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/D.C./	37	PCT Preliminary Report on Patentability (Chapter 1 of the Patent Cooperation Treaty), PCT/US2005/028793, filed 08/12/2005, mailed 12/21/2007; 2 pages	<input type="checkbox"/>
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/D.C./	39	PCT International Search Report of the International Searching Authority for PCT/US05/28793, filed 08/12/2005, mailed 11/19/2007; 5 pages	<input type="checkbox"/>
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Application Number		10001478
Filing Date		2001-11-01
First Named Inventor	Craig Nemecek	
Art Unit	2123	
Examiner Name	<del>Jason Scott Proctor</del> /Dwin Craig/	
Attorney Docket Number	CD01213M	

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Attorney Docket Number	CD01213M

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Examiner Name	<del>Jason Scott Proctor</del> /Dwin Craig/
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